

ABSTRACT OF THE DISCLOSURE

The liquid crystal display device of the present invention comprises:
pixel electrodes; a common electrode; a plurality of data lines and a plurality
5 of gate lines intersecting each other; a plurality of switchers, provided for the
pixel electrodes, for supplying signals from the data lines to the pixel
electrode; a gate line driver for scanning the gate lines; a data line driver for
driving the data lines, in accordance with the gradation to be displayed; and
a controller for controlling the gate line driver and the data line driver. The
10 controller comprises a signal absence detector for detecting that no signal
has been input to the liquid crystal display device. The controller outputs a
signal to the gate line driver to make all the gate lines active for a
predetermined time after the signal absence detector detects that no signal
has been input. The controller outputs a signal, to the data line driver, to
15 supply an electric potential, applied to the common electrode, to all the data
lines for the predetermined time.